

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Ko et al.

Serial No.: Not yet assigned

Filed: June 1, 2000

For: SEMICONDUCTOR DEVICE
HAVING A SUBSTRATE, AN UNDOPED
SILICON OXIDE STRUCTURE, AND AN
OVERLYING DOPED SILICON OXIDE
STRUCTURE WITH A SIDE WALL
TERMINATING AT THE UNDOPED
SILICON OXIDE STRUCTURE

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 3526.2US
(97-1136.2)

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Stevenson
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NOTICE OF EXPRESS MAILING

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Person making Deposit: Jared S. Turner

INFORMATION DISCLOSURE STATEMENT

Box PATENT APPLICATION
Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The present application is a continuation of application Serial No. 09/231,346, filed January 13, 1999, pending, which is a divisional of application Serial No. 09/102,152, filed June 22, 1998, pending.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior applications, and to confirm in the first Office Action on the

merits that such art has in fact been reviewed. A PTO-1449 form listing all of the information of record in the prior applications is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: June 1, 2000

BGP/jml

Enclosure: Form PTO-1449

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